

K.HARITRIVENI CV

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Email(s)
Contact Number(s)
Institution Chaitanya engineering College
Date of Birth [27/11/1986]
Gender (M/F/T) Female
Category (Gen/SC/ST/OBC) BC-A

Photo



Academic Qualifications

Degree	Year	Subject	University/Institution	%/CGPA
M.Tech	2023	VLSI SYSTEM DESIGN	JNTU HYDERABAD	7.3
B.Tech	2014	ECE	Pydah College of Engineering	58
DIPLOMA	2010	ECE	Sai Ganapati polytechnic	59
SSC	2007		BHPV HIGH School	48

PROFESSIONAL EXPERIENCE:

S.No	Position	Institute	Department	From	To
1	Technician	OPTO CIRCUITS (INDIA) LTD	OMD Production	2010 2014	2011 2015
2	Internship	ESSEN ELECTRONICS SYSTEM PVT LTD	TESTING	2015	2016
3	Trainee Assistant engineer	LINK WELL ELECTRONICS PVT LTD	VHF	2017	2018
4	Assistant Professor	VISAKHA INSTITUTE OF ENGINEERING & TECHNOLOGY	ECE	1/2023	5/2023
5	Assistant engineer	LOTUS WIRELESS TECHNOLOGIES PVT LTD	TESTING	2023	2024
6	Assistant professor	CHAITANYA ENGINEERING COLLEGE	ECE	2024	TILL NOW

Technical Skills

Skill / Technology		
Programming	MS office, C - Language	AUTO CAD
Software Tools	Cadence Or CAD 16.6.	ZUKEN Cadence
Core Skills	Machine Learning	Deep Learning
Domain Knowledge	Low Power VLSI Design	FPGA Design